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(Use several sheets if necessary)

Jeng H. Hwang et al. Applicants

December 22, 2000 Filing Date

<u>1746</u> 1765 Group

## U. S. PATENT DOCUMENTS

Examiner Initial	Document Number	Issue Date	Name	Class	Subclass	Filing Date  If Appropriate
<u>5A</u>	5,186,718	02/16/93	Tepman et al.	29	25.01	
SA	5,789,320	08/04/98	Andricacos et al.	438	678	
SA	6,030,666	02/29/00	Lam et al.	427	539	
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SA	6,087,265	07/11/00	Hwang	438	706	- 0

Examiner

Date Considered

9/4/02

Shamin Ahmed

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## FOREIGN PATENT DOCUMENTS

Document Number	Publication <u>Date</u>	Name	Class	Subclass	Translation If Appropriate
DD 142966	07/23/80	Schade et al.	C23C	15/00	Abstract
DD 285224	12/05/90	Geiler et al.	H01L	21/265	Abstract
JP 53109475	09/25/78	Hitachi Ltd.	H01L	21/203	Abstract
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Chow, T.P. and A. J. Steckl\*. "Plasma Etching of Refractory Gates for VLSI Applications." J. Electrochem. Soc. (Vol. 131, No. 10). October 1984. pp. 2325-2335.

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